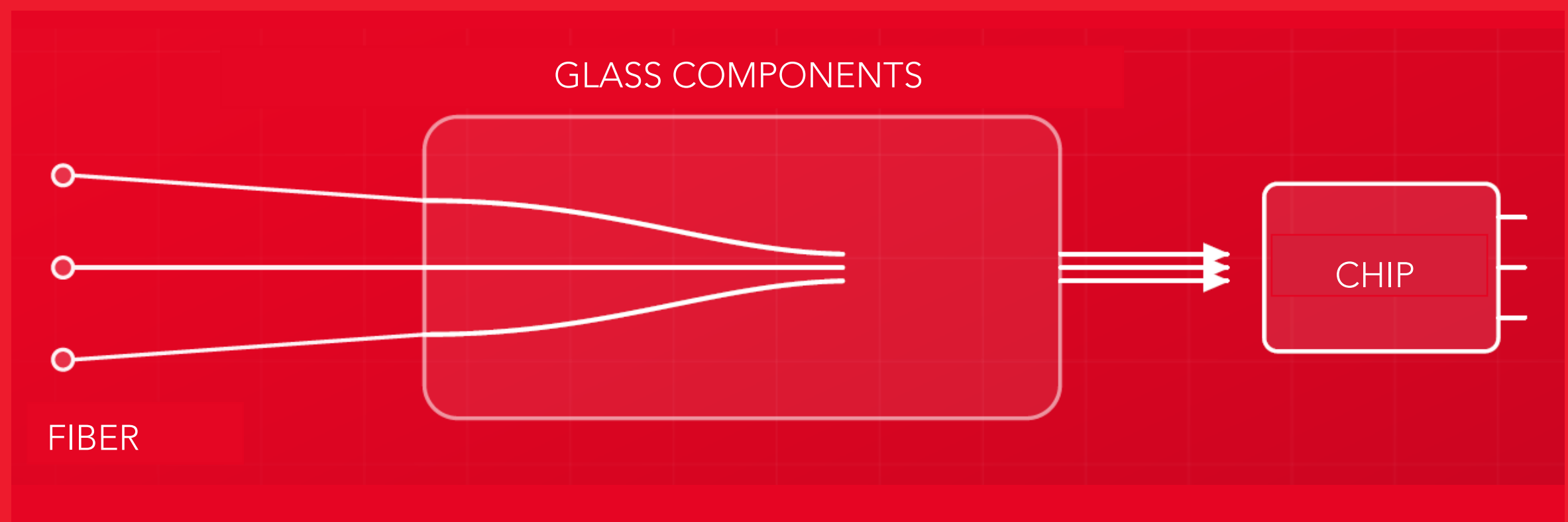
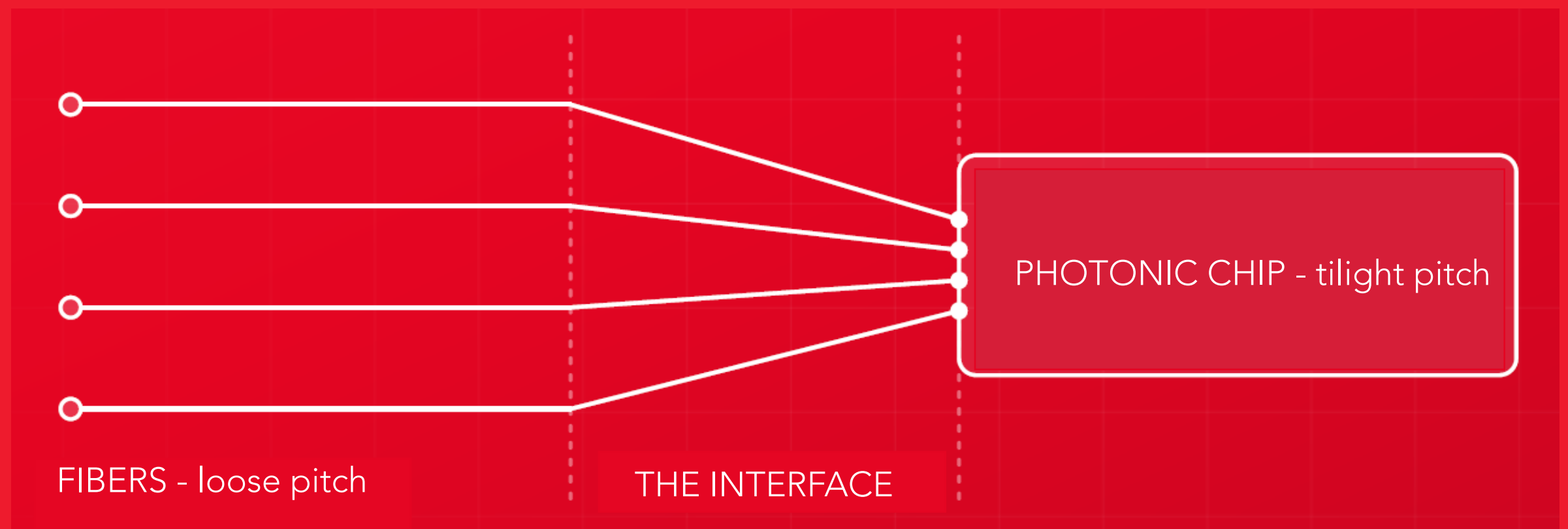


# The fiber-to-chip bottleneck.

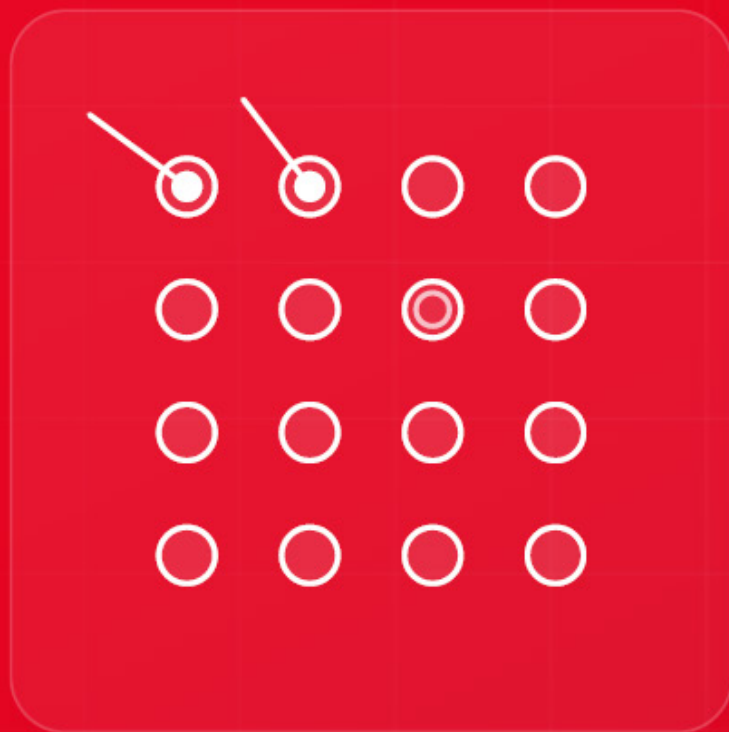


➡ 3 parts, one glass component.

# One interface. Where cost, yield & density collide.

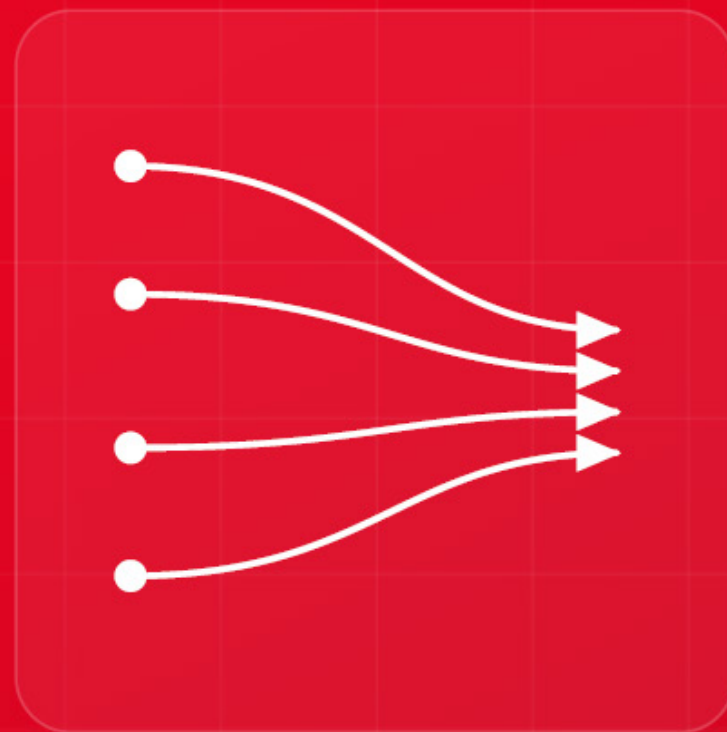


# Three glass building blocks



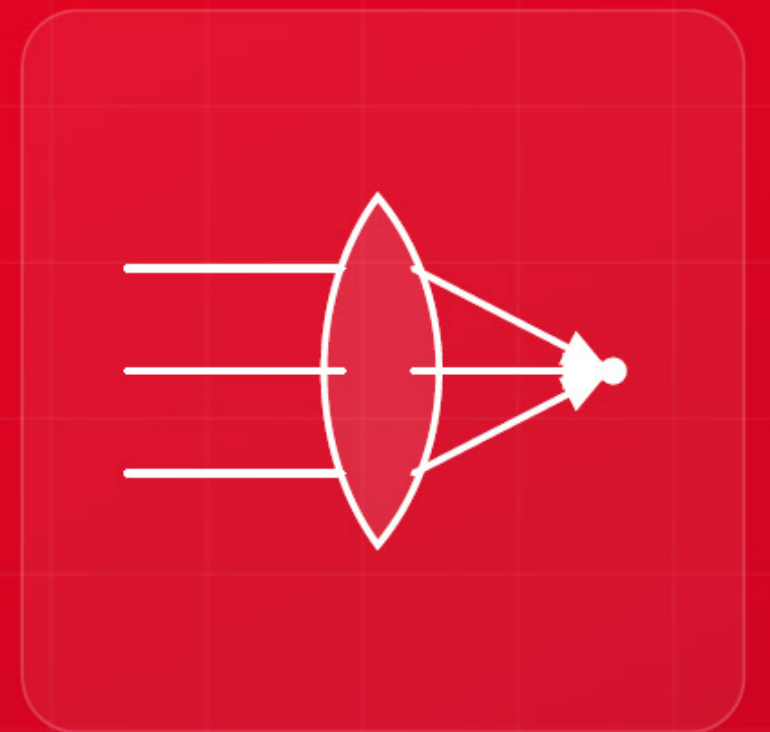
## Hole arrays

2D fiber ferrules ·  $\pm 0.5 \mu\text{m}$



## 3D waveguides

true 3D routing · 0.3 dB/cm

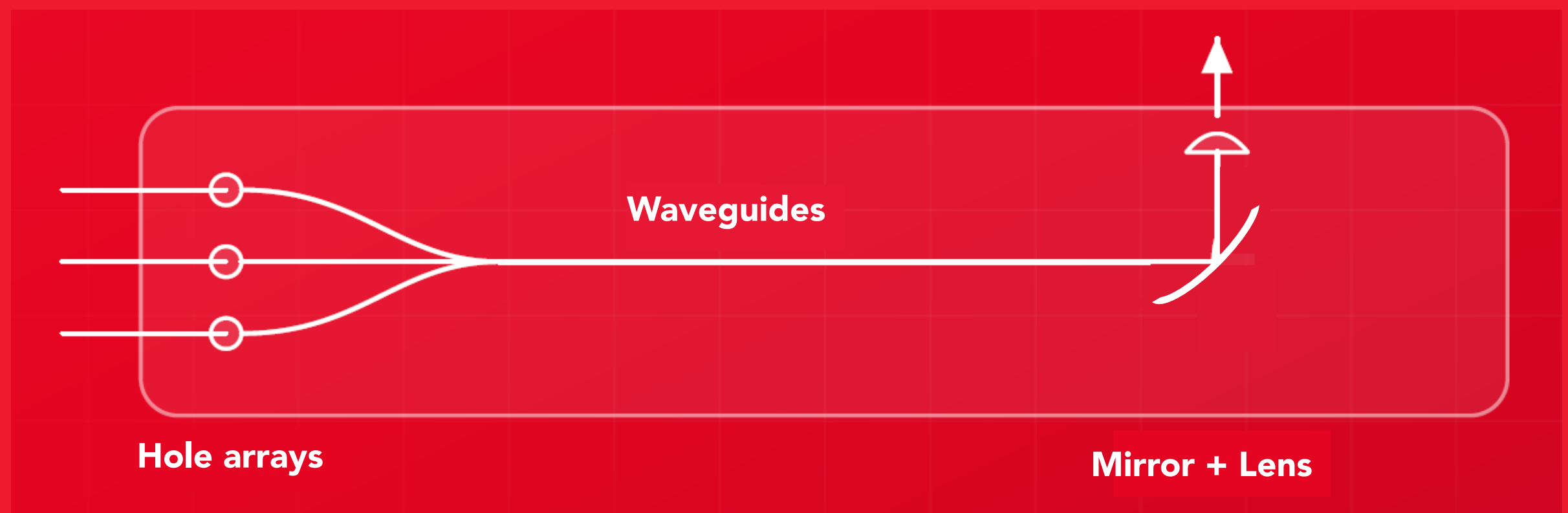


## Free-form optics

lenses & mirrors · Ra 5 nm

➔ The real advantage.

# All three. One monolithic part.



$\pm 1 \mu\text{m}$  relative

5 optical functions

↓ cost / function

➡ Let's build it.

# Building fiber-to-chip connectivity?

Contact us

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